

Digital Logic Question Paper

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question paper, EC6302 apr may 2017 question paper, ec6302 apr may 2018 question paper, EC6302 Apr/May ... EC6302 Digital Electronics Previous Year Question Papers ... (b) Question 10 (b) asks you to write a logic statement for the given diagram For this question 1 mark would be awarded for each correct part of the answer. Answer The whole answer should look something like this: $X = (A \text{ AND } B) \text{ AND } (C \text{ OR NOT } B)$ 1 mark for each of the following sections (A AND B) = 1 mark AND = 1 mark (C OR NOT B) = 1 mark Exam Questions - Logic Gates - Bits of Bytes.co Download link is provided below to ensure for the Students to download the Regulation 2017 Anna University EE8351 Digital Logic Circuits Lecture Notes, Syllabus, Part-A 2

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outputs and number of outputs is less than inputs, it is a Priority encoder $V=1$ when input is valid and for priority encoder it checks first high bit encountered. Except all are having at least one bit high and 'x' represents the "don't care" as we have found a high bit already. Digital Logic & Number representation -

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